

FIG. 1

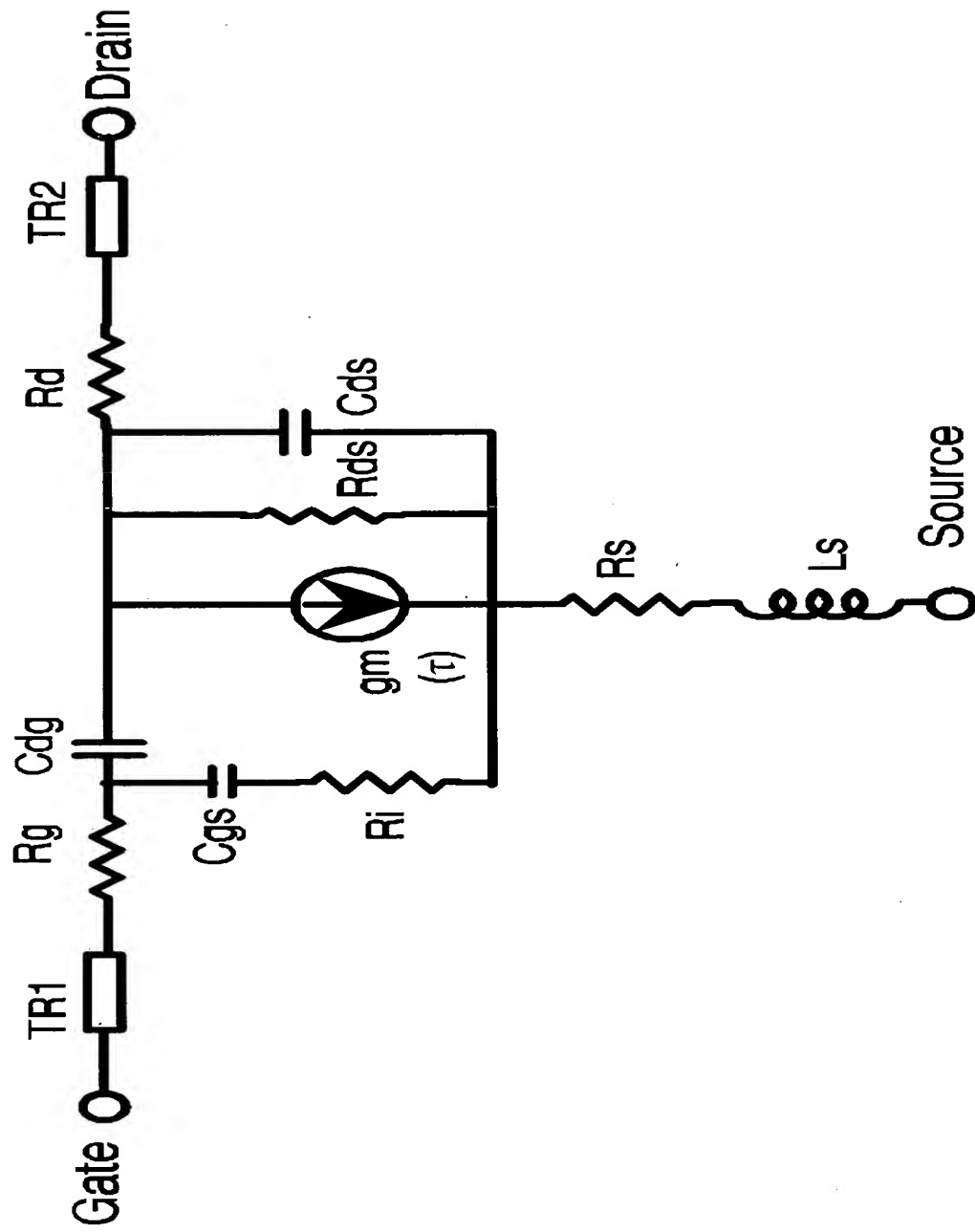


FIG. 1a

Vp (V)	BVgd (V)	Ids (mA/mm)	Gmax (mS/mm)	Imax (mA/mm)
Power	-1.0 ± 0.050	>12	450	>350
D-mode	-1.0 ± 0.050	>6	450	>450
E-mode	+0.1 ± 0.050	>6	160	>500

Ft (GHz)	Fmax (GHz)	NF (dB)	Rds (ohm-mm)	Gm (mS/mm)
Power	>30	>100	N/A	>28
D-mode	>50	>100	<0.8	>28
E-mode	>50	>100	<0.8	>28

FIG. 1b

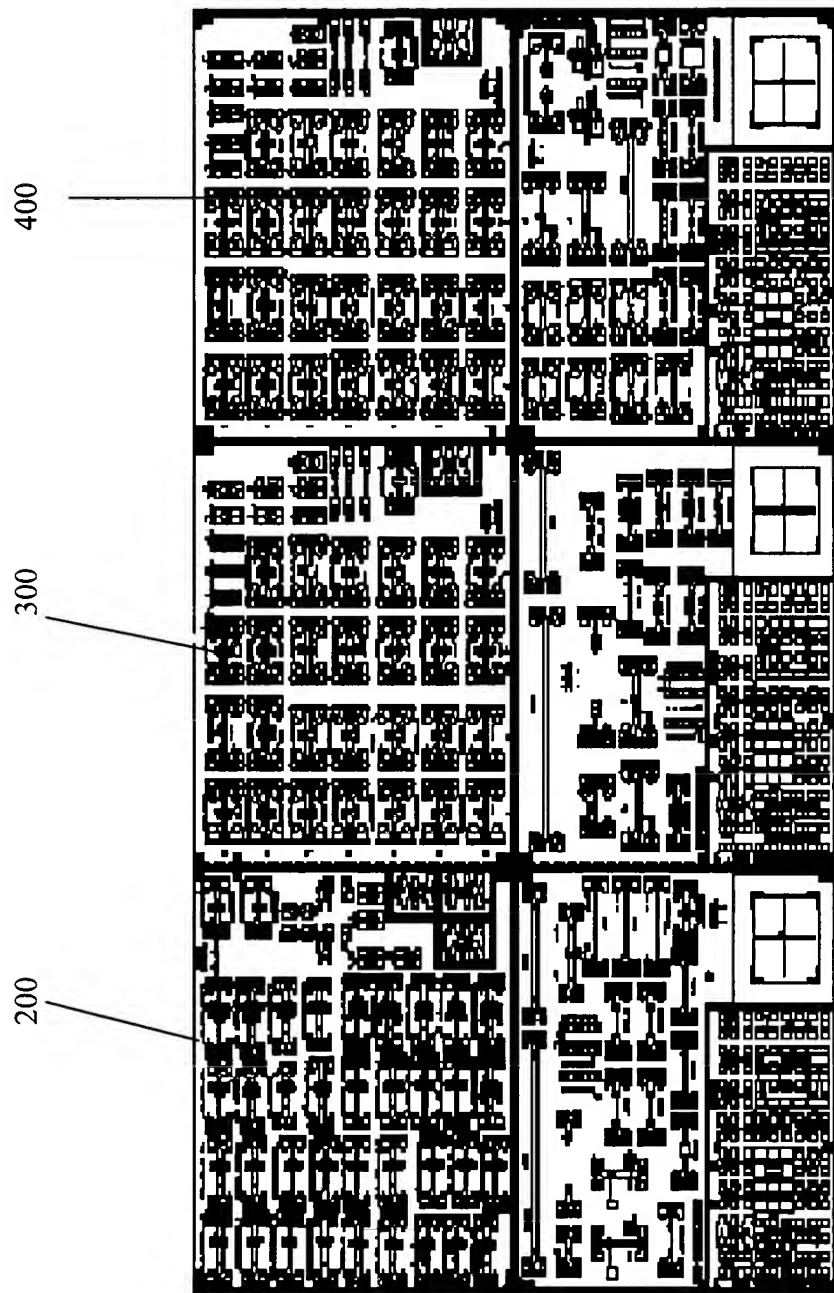
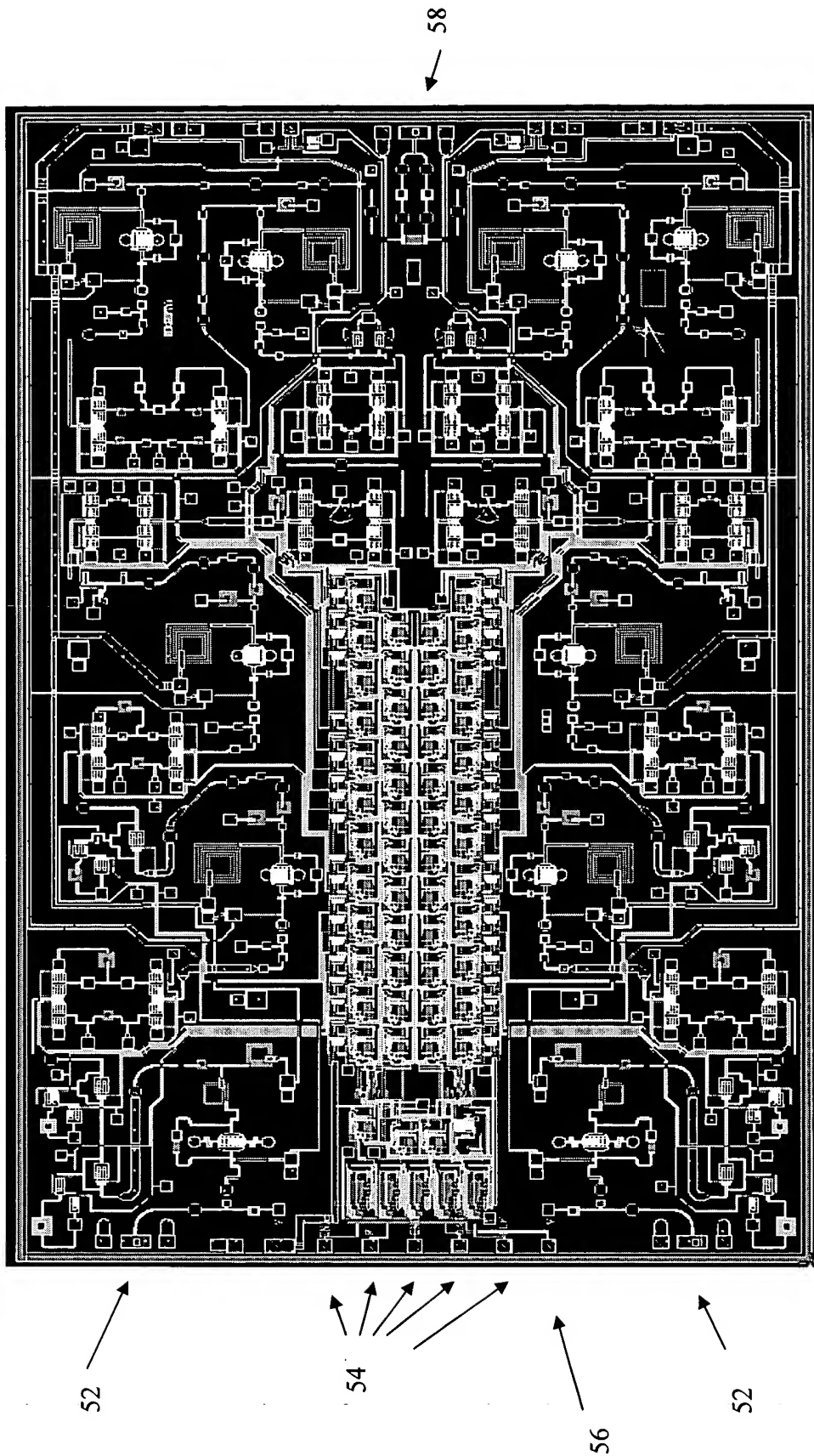


FIG. 2



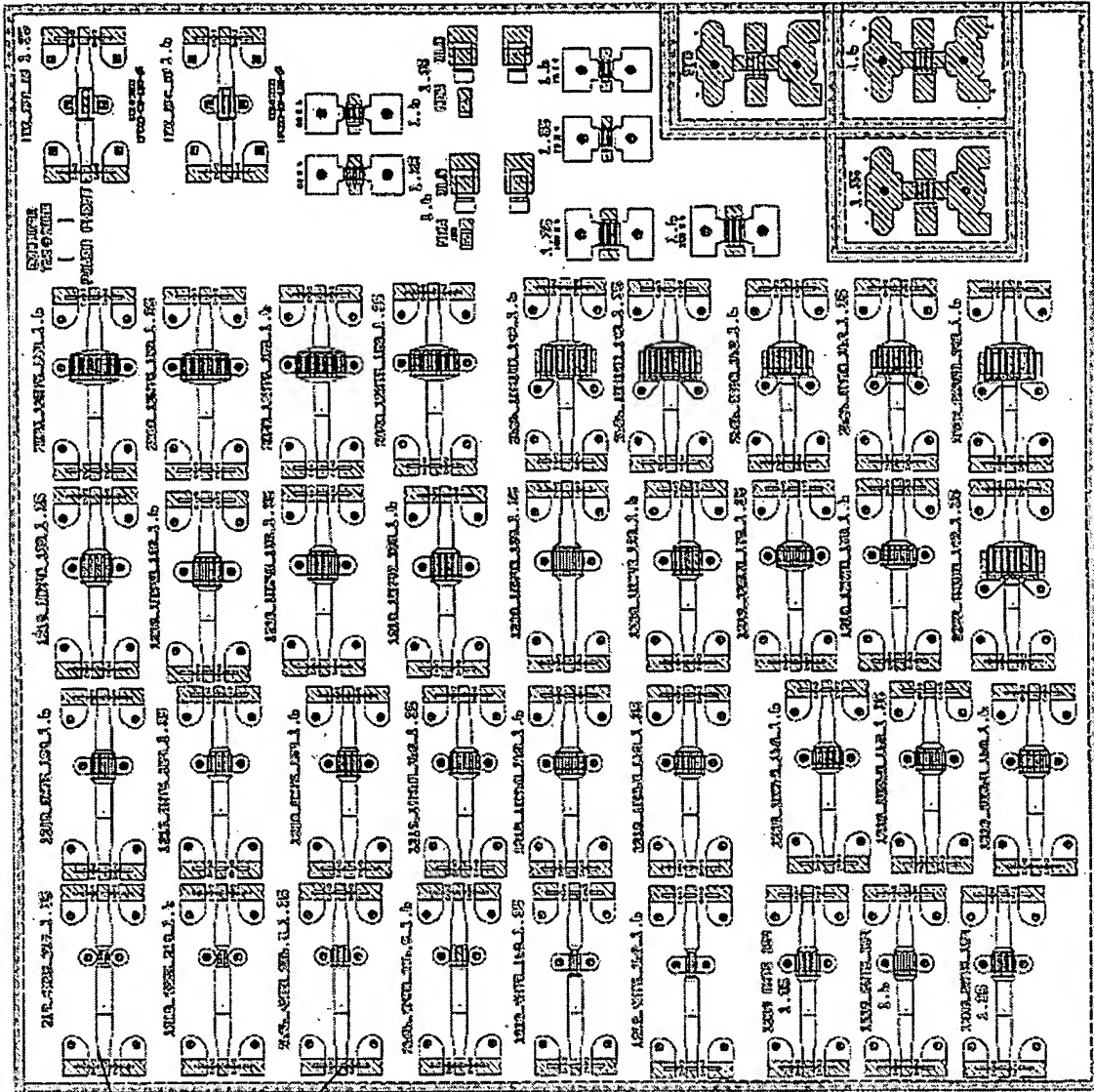


FIG. 3

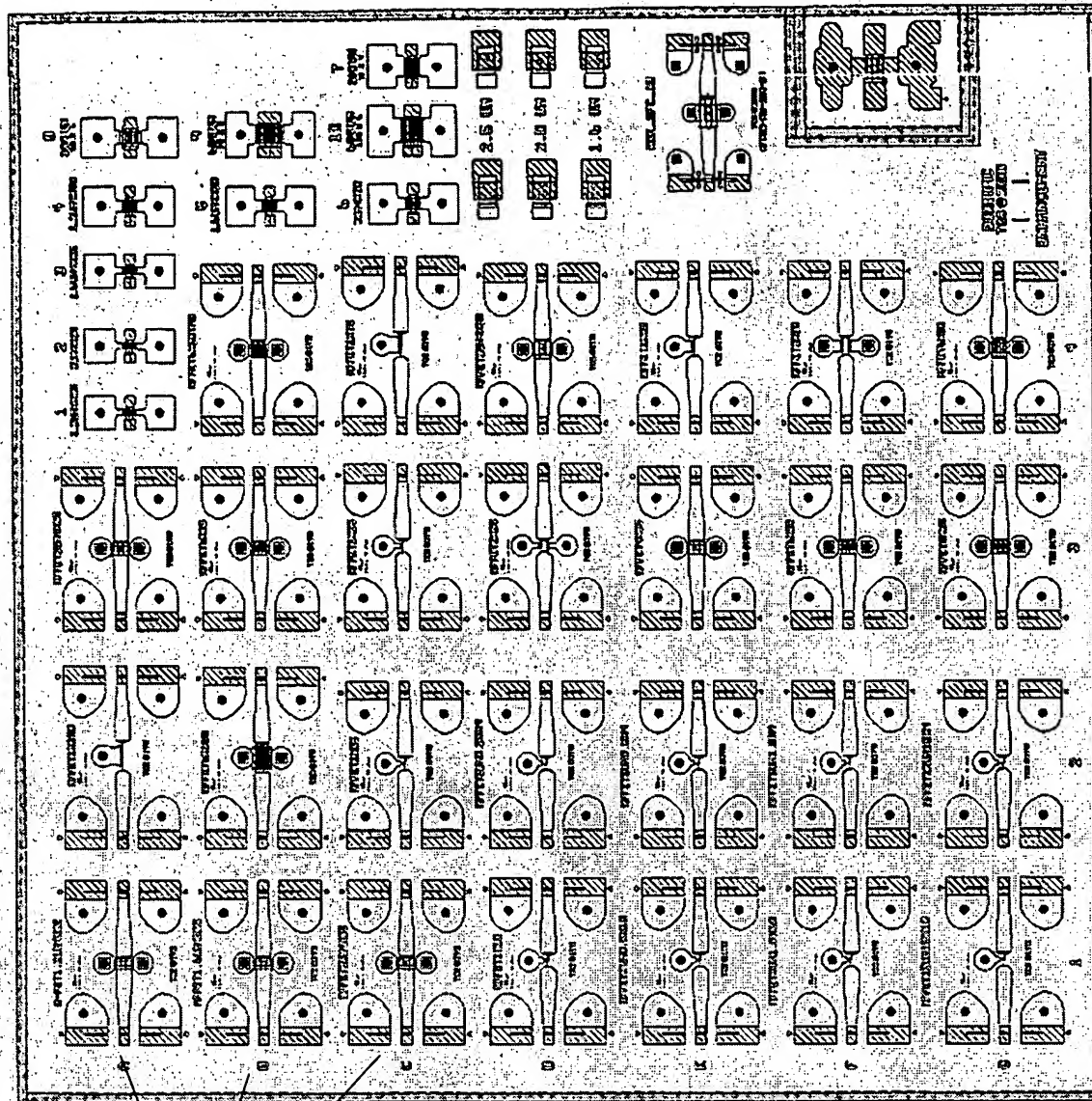


FIG. 4

GAIN STAGE V8



3 INPUT AND CELL

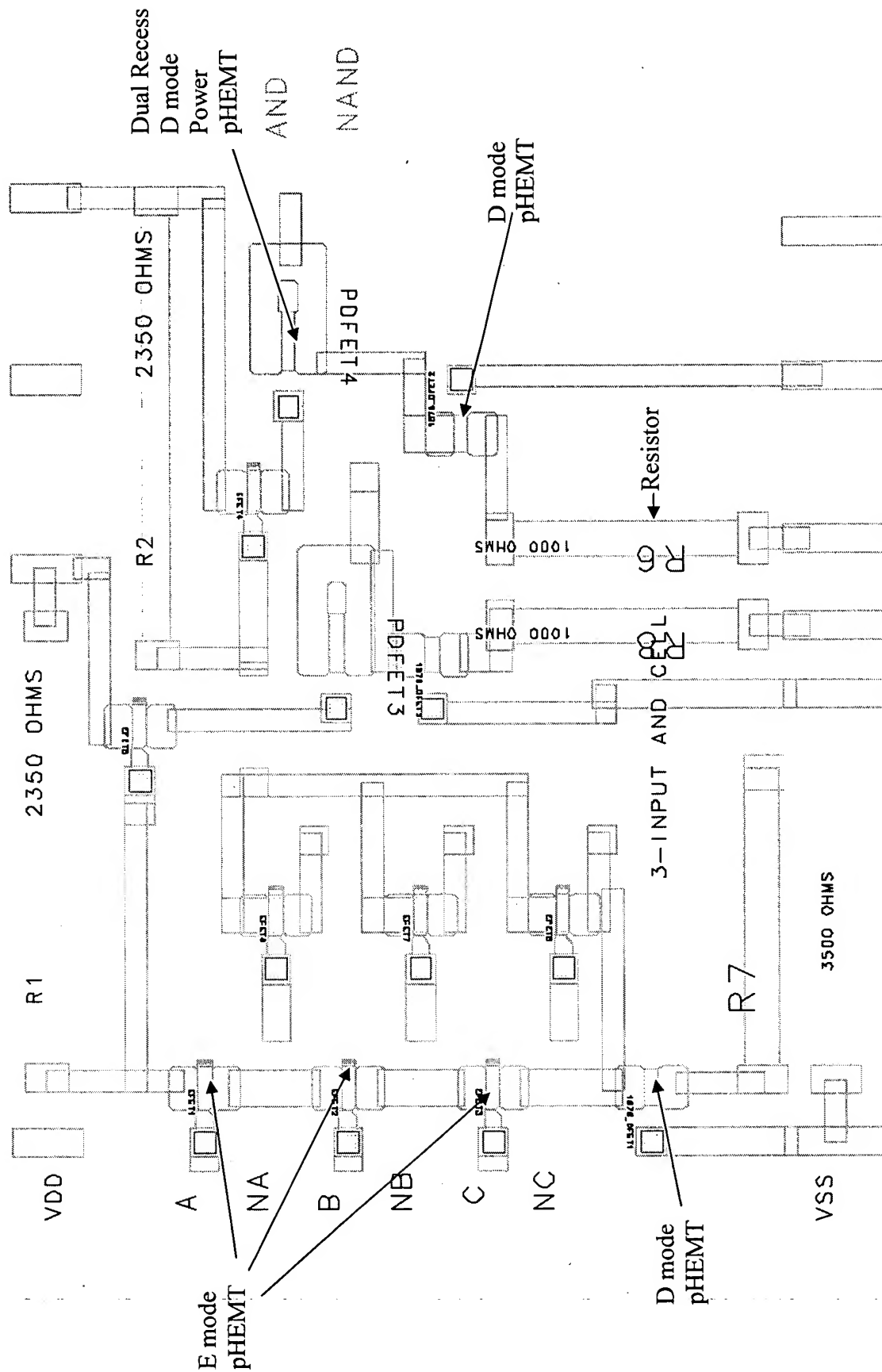


FIG. 5b

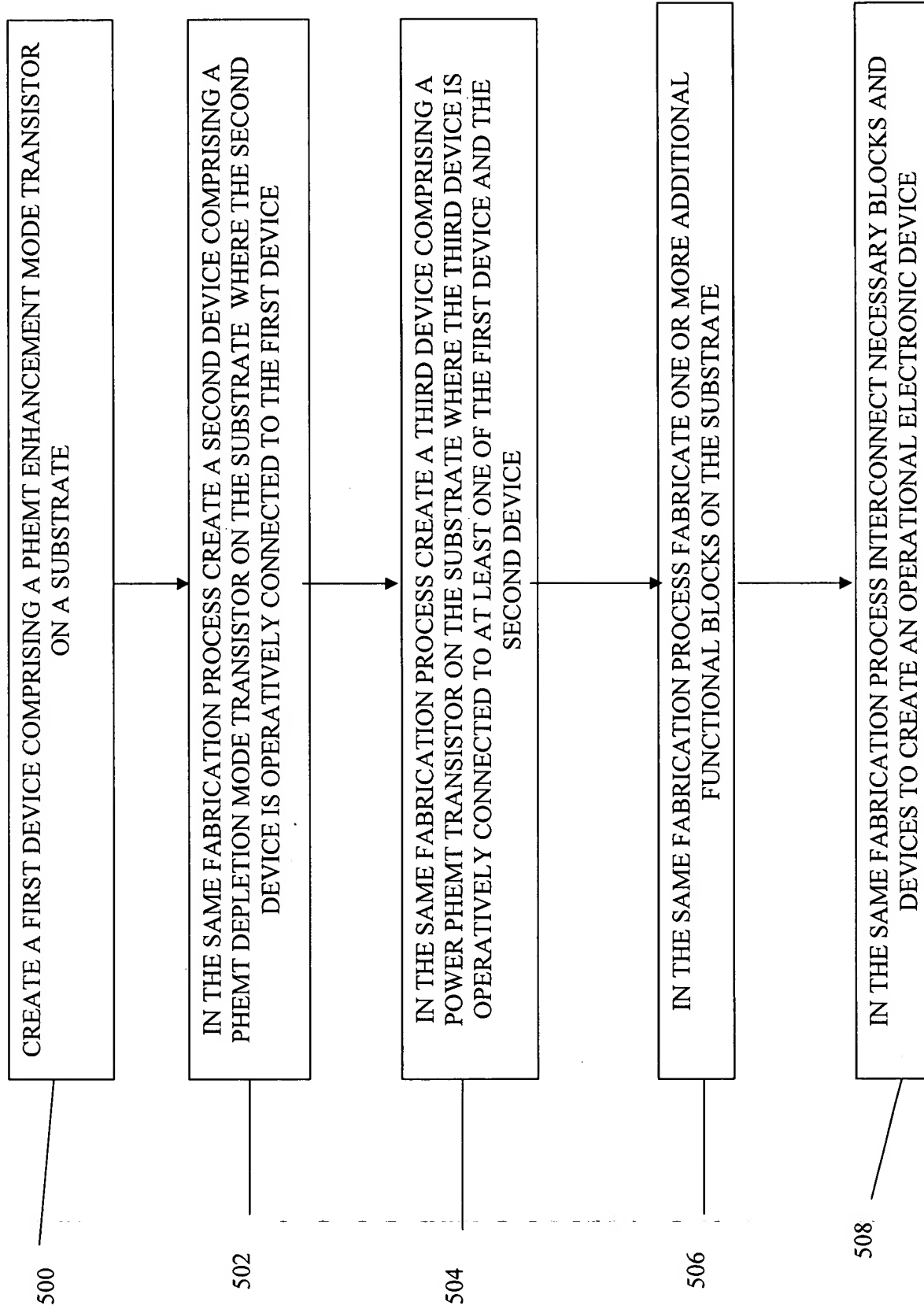


FIG. 6

HDI Power/E/D PHEMT Process Flow

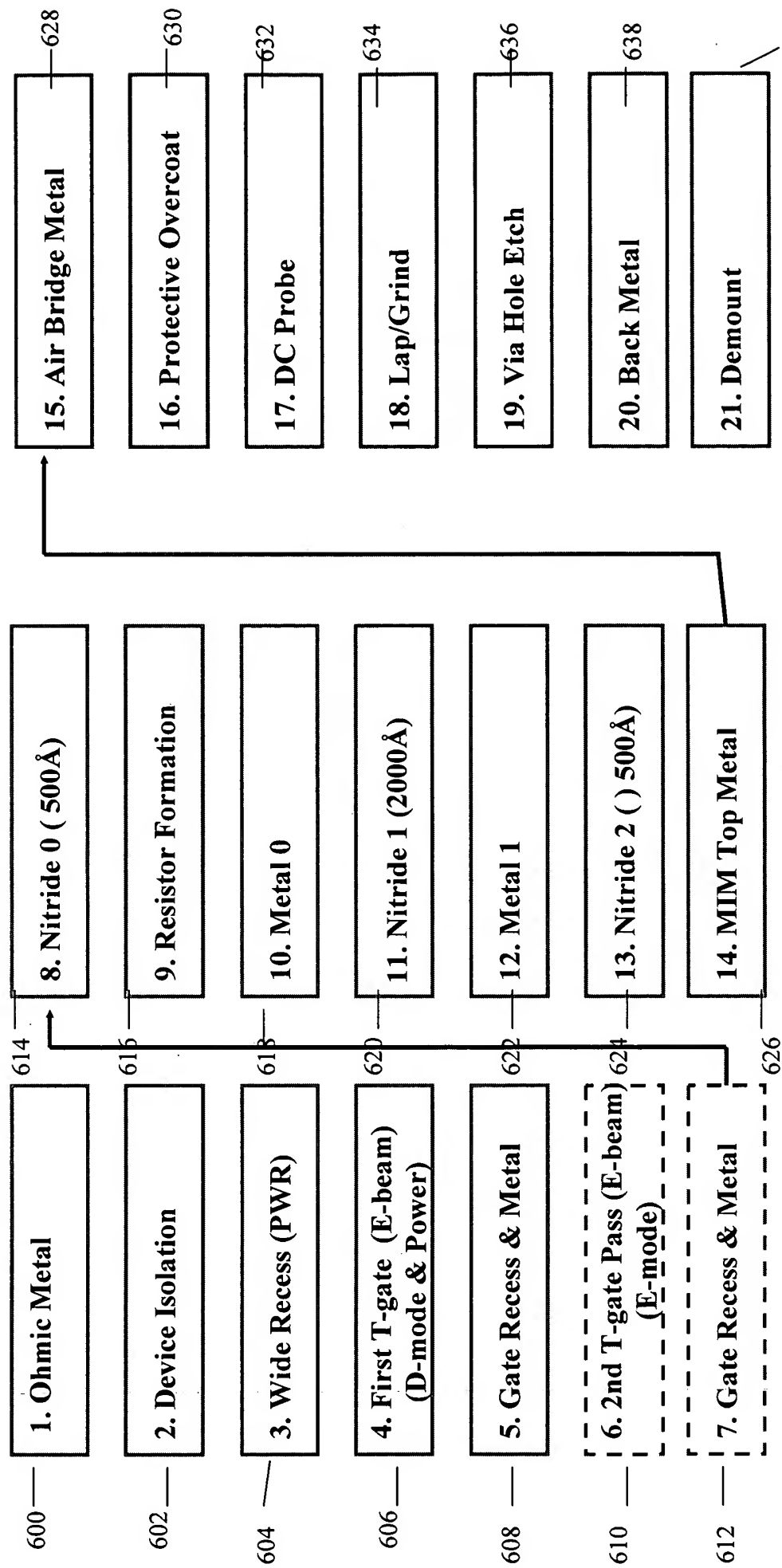


FIG. 7